

UNIVERSITY COLLEGE OF ENGINEERING (A)
OSMANIA UNIVERSITY, HYDERABAD – 500007
B.E. (BME) - II- SEMESTER (MAIN) PRACTICAL EXAMS TIME TABLE – JUNE -2026

Name of the Lab:

(RE-REVISED)

Date	Time	Hall Tickets Nos.	From ---- To	Name of the Lab
09.06.2026 (Tuesday)	08.00 – 11.00 hrs.	1005-25-731001 to 1005-25-731022		Computer Aided Engineering Lab
	11.00 – 14.00 hrs.	1005-25-731023 to 1036, 1038 to 1044, 1046		
	14.00 – 17.00 hrs.	1005-25-731048, 1050 to 1067, 1005-24-731053, 1054, 1066 & Backlog		
12.06.2026 (Friday)	08.00 – 11.00 hrs.	1005-25-731001 to 1005-25-731022		Communicative English Lab
	11.00 – 14.00 hrs.	1005-25-731023 to 1036, 1038 to 1044, 1046		
	14.00 – 17.00 hrs.	1005-25-731048, 1050 to 1067, 1005-24-731053, 1054, 1066 & Backlog		
15.06.2026 (Monday)	08.00 – 11.00 hrs.	1005-25-731001 to 1005-25-731008		Basic Electrical Engineering Lab
	11.00 – 14.00 hrs.	1005-25-731009 to 1005-25-731016		
	14.00 – 17.00 hrs.	1005-25-731017 to 1005-25-731024		
	08.00 – 11.00 hrs.	1005-25-731025 to 1005-25-731031		Programming for Problem Solving Lab
	11.00 – 14.00 hrs.	1005-25-731032 to 1036, 1038, 1039		
	14.00 – 17.00 hrs.	1005-25-731040 to 1044, 1046, 1048		
	08.00 – 11.00 hrs.	1005-25-731050 to 1005-25-731056		Electronic Devices and Circuits Lab
	11.00 – 14.00 hrs.	1005-25-731057 to 1005-25-731063		
14.00 – 17.00 hrs.	1005-25-731064 to 1067, 1005-24-731053, 1054, 1066 & Backlog			
16.06.2026 (Tuesday)	08.00 – 11.00 hrs.	1005-25-731050 to 1005-25-731056		Basic Electrical Engineering Lab
	11.00 – 14.00 hrs.	1005-25-731057 to 1005-25-731063		
	14.00 – 17.00 hrs.	1005-25-731064 to 1067, 1005-24-731053, 1054, 1066 & Backlog		
	08.00 – 11.00 hrs.	1005-25-731001 to 1005-25-731008		Programming for Problem Solving Lab
	11.00 – 14.00 hrs.	1005-25-731009 to 1005-25-731016		
	14.00 – 17.00 hrs.	1005-25-731017 to 1005-25-731024		
	08.00 – 11.00 hrs.	1005-25-731025 to 1005-25-731031		Electronic Devices and Circuits Lab
	11.00 – 14.00 hrs.	1005-25-731032 to 1036, 1038, 1039		
14.00 – 17.00 hrs.	1005-25-731040 to 1044, 1046, 1048			
17.06.2026 (Wednesday)	08.00 – 11.00 hrs.	1005-25-731025 to 1005-25-731031		Basic Electrical Engineering Lab
	11.00 – 14.00 hrs.	1005-25-731032 to 1036, 1038, 1039		
	14.00 – 17.00 hrs.	1005-25-731040 to 1044, 1046, 1048		
	08.00 – 11.00 hrs.	1005-25-731050 to 1005-25-731056		Programming for Problem Solving Lab
	11.00 – 14.00 hrs.	1005-25-731057 to 1005-25-731063		
	14.00 – 17.00 hrs.	1005-25-731064 to 1067, 1005-24-731053, 1054, 1066 & Backlog		
	08.00 – 11.00 hrs.	1005-25-731001 to 1005-25-731008		Electronic Devices and Circuits Lab
	11.00 – 14.00 hrs.	1005-25-731009 to 1005-25-731016		
14.00 – 17.00 hrs.	1005-25-731017 to 1005-25-731024			

Note:

- Any omission or clash in the practicals timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.
- These practical time table is also applicable for the backlog/supplementary students whose numbers are not listed above.


Chief Superintendent (Exams)
UCE (A), OU.

Chief Superintendent
Univ. College of Engineering
Osmania University, Hyd-7.